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DCCA201

Reg. No.

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II Semester B.C.A. Degree Examination, July/August - 2024

**COMPUTER SCIENCE****Computer (Applications) Architecture**

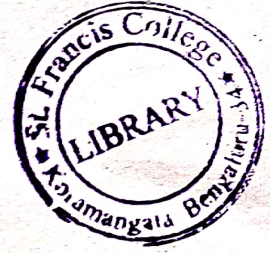
(Scheme NEP)

Time : 2½ Hours

Maximum Marks : 60

*Instructions to Candidates :* Attempt all Sections.**SECTION - A****I. Answer any Four questions. Each question carries 2 marks. (4×2=8)**

1. Define computer Architecture.
2. Convert  $(642)_{10}$  to Binary.
3. Convert  $11011_{(2)}$  to Gray code.
4. What is Micro operation?
5. Define cache memory
6. What are peripheral devices?

**SECTION - B****II. Answer any Four questions. Each question carries 5 marks. (4×5=20)**

7. Simplify  $F(A,B,C,D) = \sum m(1,5,7,8,9,13) + \sum d(3,12)$
8. With neat diagram explain 4-to-1 multiplexer.
9. Explain Associative memory.
10. With neat diagram and truth table explain Half Adder and Full Adder.
11. Explain BUN and BSA Instructions.
12. Write the characteristics of CISC and RISC.

**[P.T.O.]**



## SECTION - C

III. Answer any Four questions. Each question carries 8 marks.

(4×8=32)

13. What is Decoder? Explain 3×8 Line Decoder with Truth Table.
  14. Explain types of CPU organization.
  15. Explain Interrupt cycle with flow chart.
  16. What is DMA? Explain its working with neat diagram.
  17. Explain the various addressing modes in detail.
  18. Explain Multithreaded Architecture.
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